Electronic Acknowledgement Receipt				
EFS ID:	5132241			
Application Number:	10755042			
International Application Number:				
Confirmation Number:	8665			
Title of Invention:	Integrated chip package structure using silicon substrate and method of manufacturing the same			
First Named Inventor/Applicant Name:	Mou-Shiung Lin			
Customer Number:	27765			
Filer:	Winston Hsu			
Filer Authorized By:				
Attorney Docket Number:	MEGP0004USA1			
Receipt Date:	10-APR-2009			
Filing Date:	09-JAN-2004			
Time Stamp:	06:04:37			
Application Type:	Utility under 35 USC 111(a)			

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Payment was successfully received in RAM	\$940
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		2ee6d6ede3a55e494d2cfae3ce1f5742a350 8c31			
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2 Extension of Time	MEGP0004-1-A2-11-SB22.pdf	66248	no	1	
		0a26b122d2517993b0484104821568af629 1c2bf			
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Amendment Submitted/Entered with	AAECDOOGALICAA DA A AAA JE	153398			
3	Filing of CPA/RCE	MEGP0004USA1_R1_4_AM.pdf	0e65af2c07ba63e9402aa6549a03f0a8c2a5 e802	no	1
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4 Claims	MEGP0004USA1_R1_4_CLAM. pdf	250239	no	7	
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Applicant Arguments/Remarks Made in an Amendment	MEGP0004USA1_R1_4_RM.pdf	249910	no	13	
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6 Fee Worksheet (PTO-06)	fee-info.pdf	32122	no	2	
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